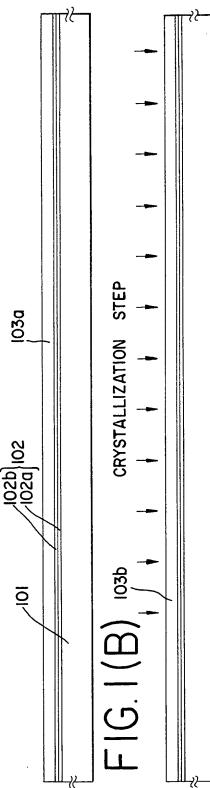
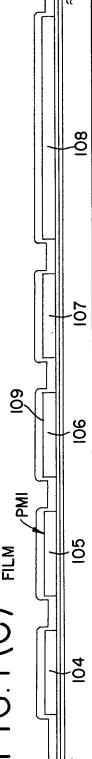
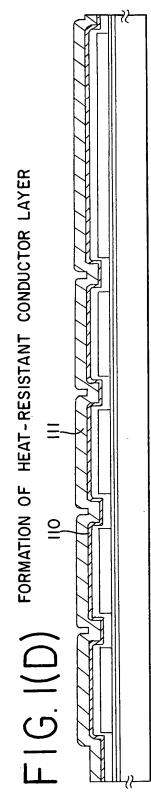


 $\mathsf{F}(\mathsf{G},\mathsf{I}(\mathsf{A})$  formation of underlying film/amorphous semiconductor film

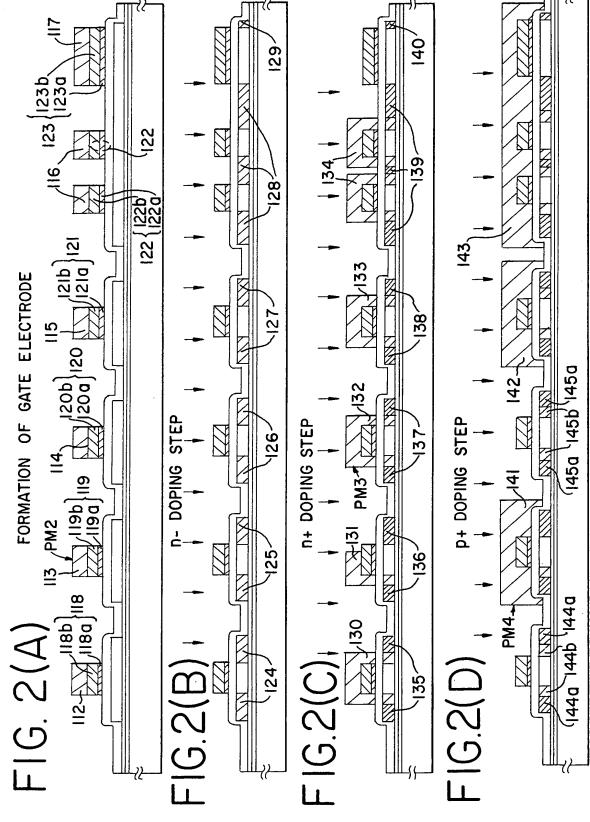


FORMATION OF ISLAND SHAPE SEMICONDUCTOR LAYER/GATE INSULATION FILM <u>60</u> FIG. I (C)

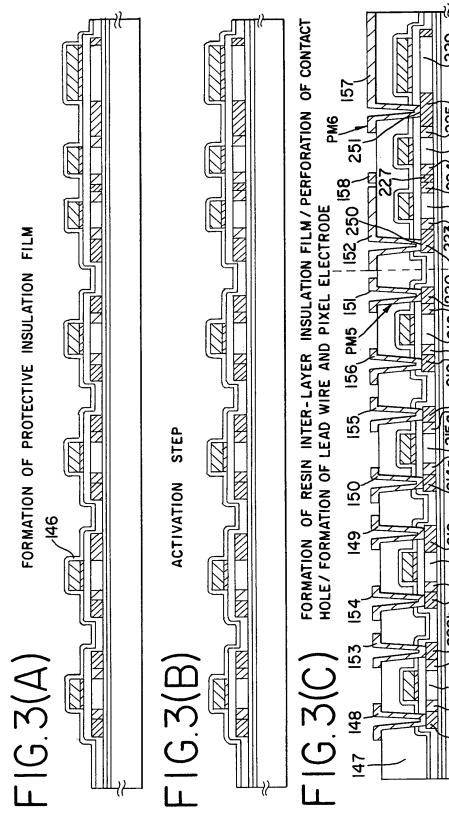


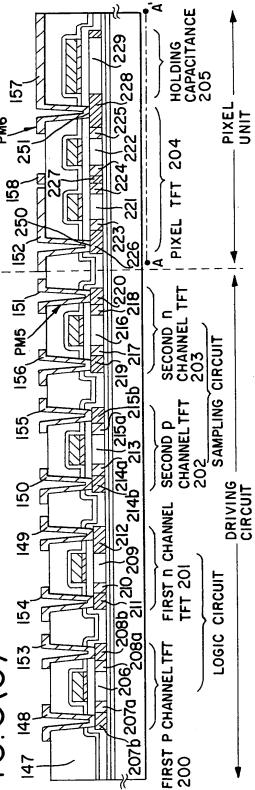




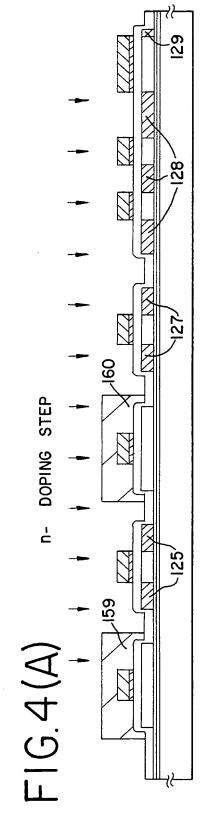


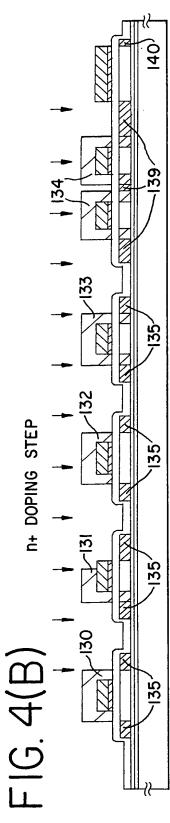


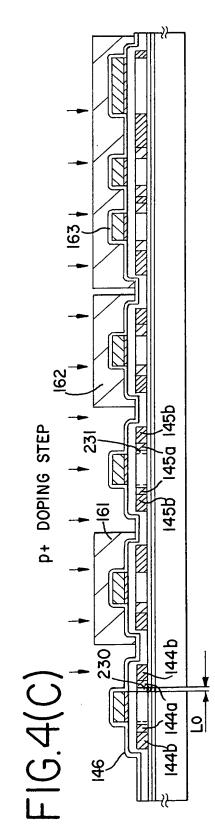




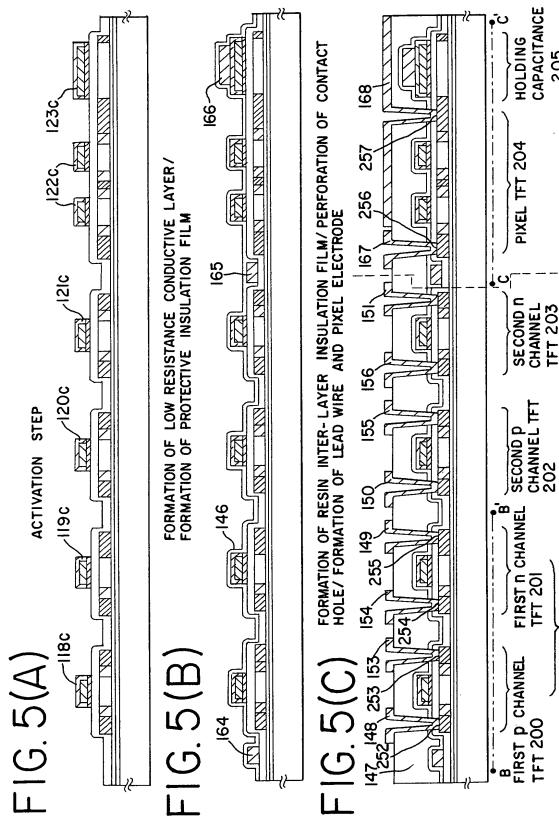












CAPACITANCE

205

TFT 203

SAMPLING CIRCUIT

DRIVING

CIRCUIT

LOGIC

PIXEL UNIT



FIG. 6(A)

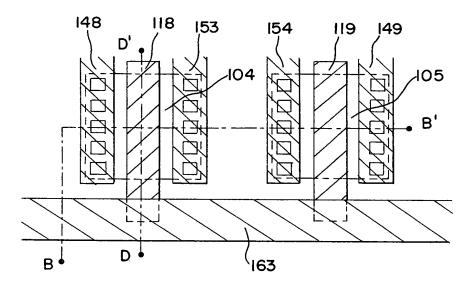
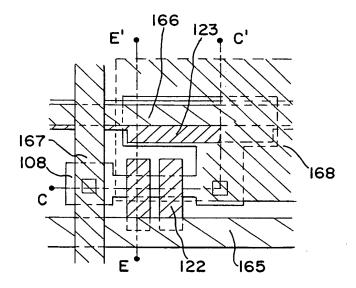
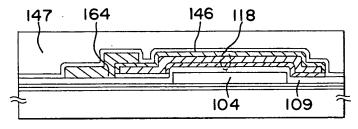


FIG. 6(B)

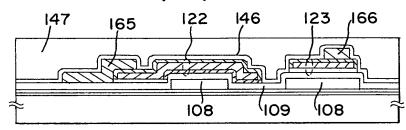




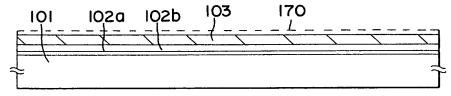
# FIG. 7(A)



## FIG. 7(B)



## FIG.8(A)



# FIG.8(B)

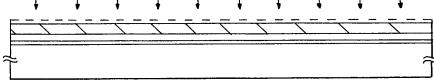
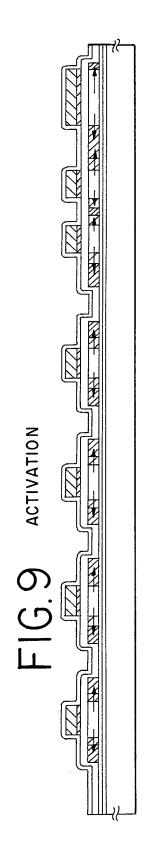
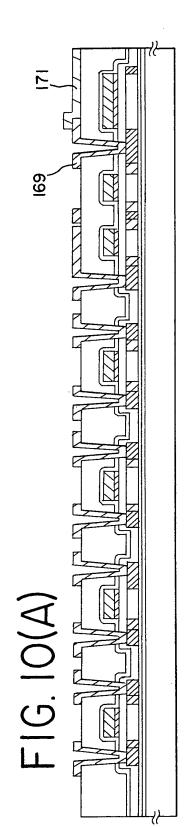
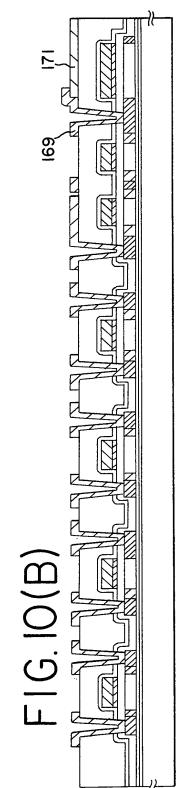


FIG.8(C) 103c

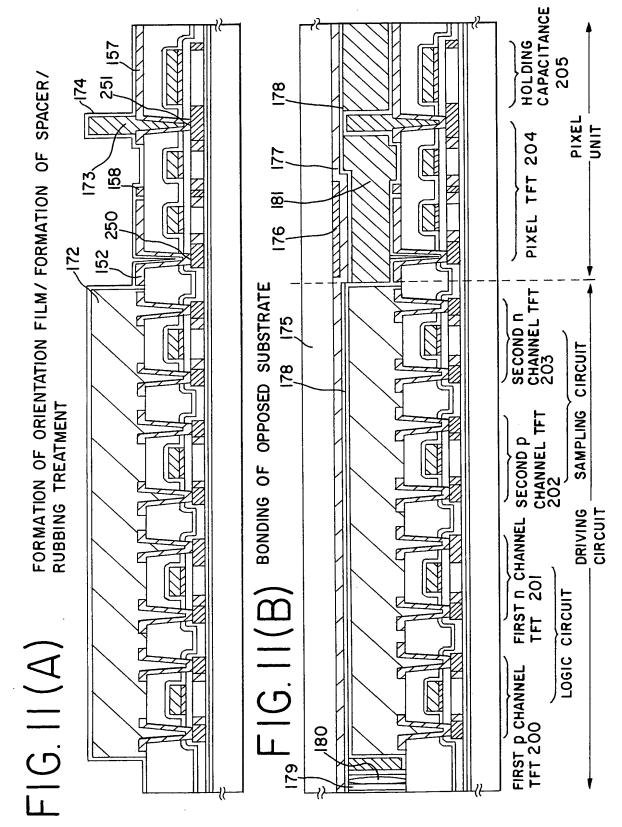




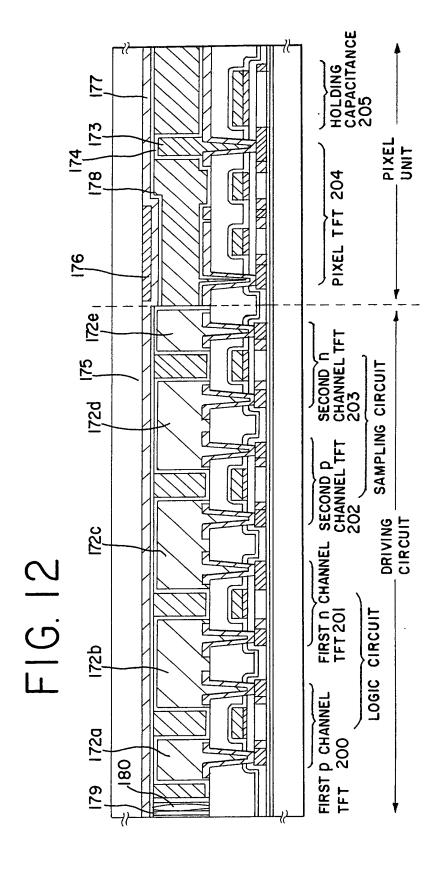




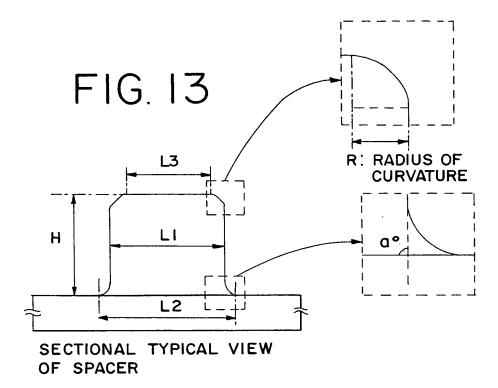


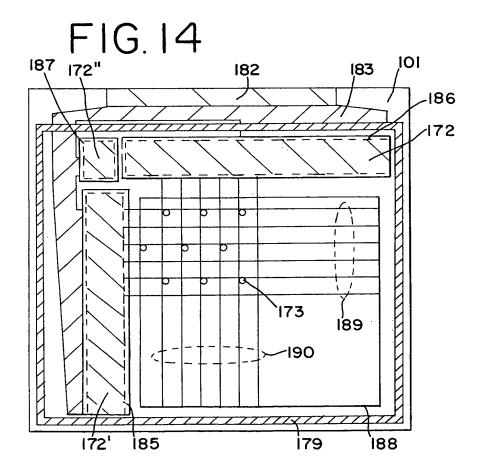




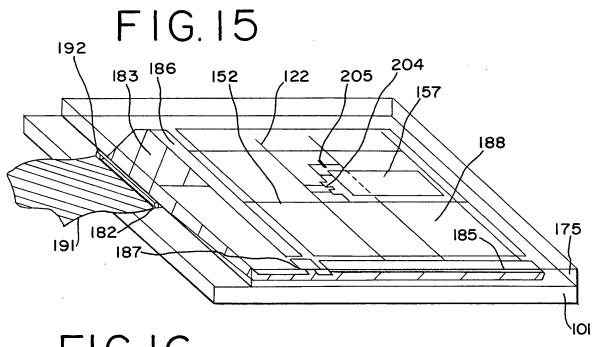


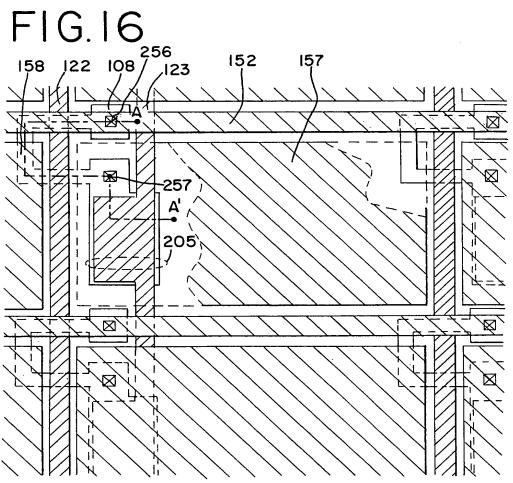




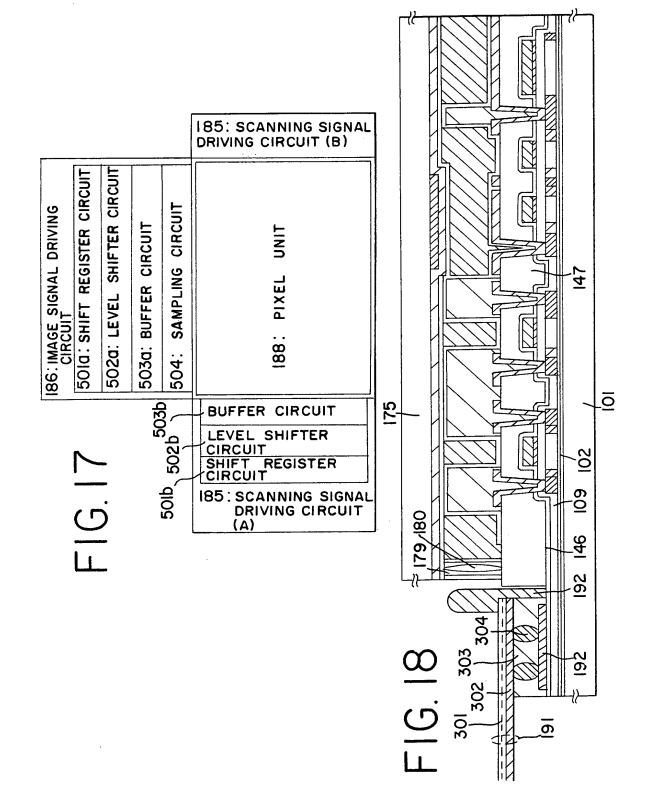




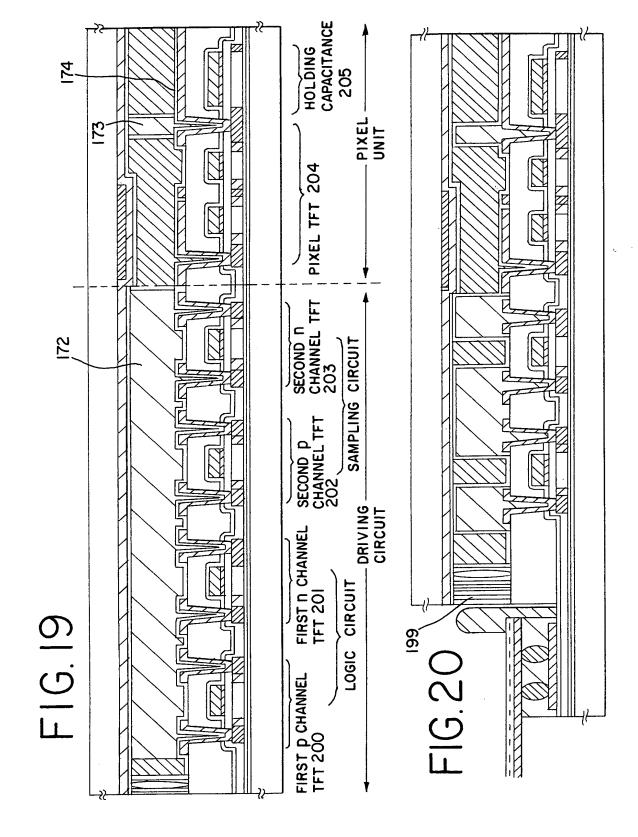








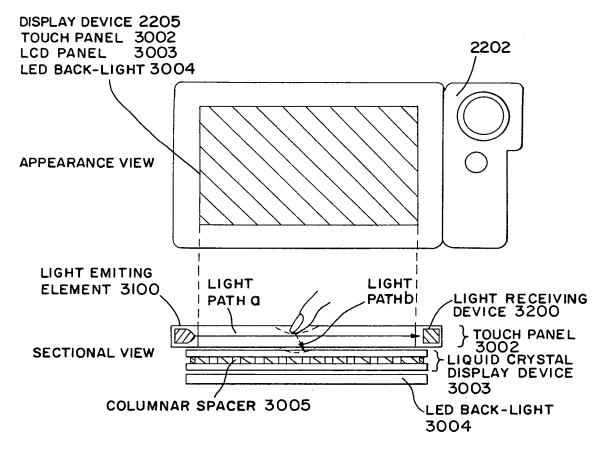






#### FIG.21(A)

APPEARANCE VIEW AND SECTIONAL VIEW OF PORTABLE INFORMATION TERMINAL (OPTICAL TOUCH PANEL)



### FIG.2I(B)

SECTIONAL VIEW OF PIXEL UNIT

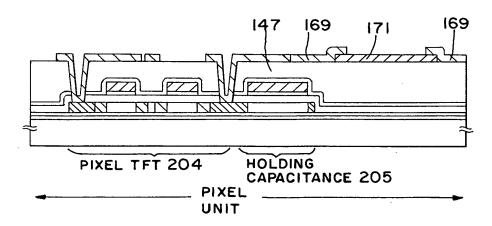




FIG.22(A)

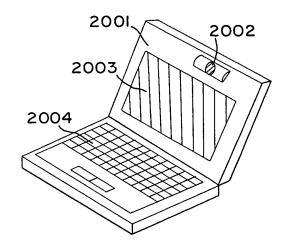


FIG.22(C)

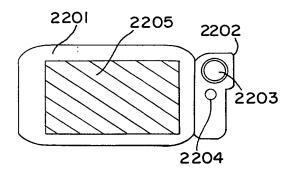


FIG. 22(E)

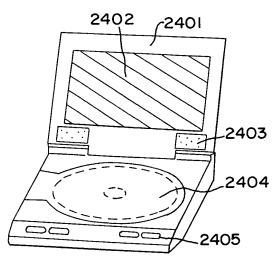


FIG. 22(B)

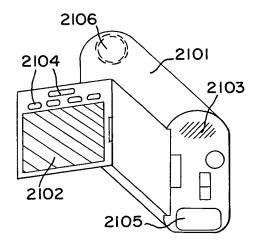


FIG.22(D)

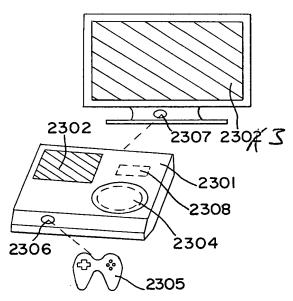


FIG.22(F)

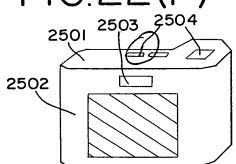
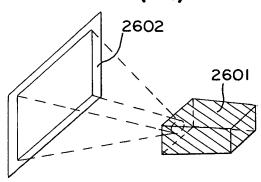




FIG. 23(A)

FIG.23(B)



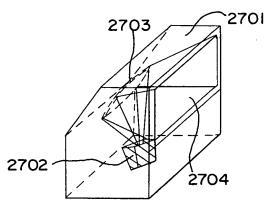


FIG. 23(C)

